000000000 000000000 0000000000 000 000 000 000	PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC	000000000 000000000 0000000000 000 000 000 000	MMM MMM MMM MMM MMMMM MMMMM MMMMMM MMMMMM

_\$2

Sym

ASC

BOD BOD BOD BOD BOD BUG BYP CAN CAN CHE

CLU

000000 00 00 00 00	PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	RRRRRRRR RR	NN	AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA	B8888888 B8888888 B8 B8 B8 B8 B8 B8 B8 B8 B888888 B8		
		\$					

OPO

V03-002 CWH3002

CW Hobbs

Various and sundry things to make OPCOM distributed

(1)

VAX-11 Bliss-32 V4.0-742 [OPCOM.SRCJOPRENABLE.B32:1

16-Sep-1983

30-Jul-1983

OP

VO

OP

```
K 3
16-Sep-1984 01:41:12
14-Sep-1984 12:50:52
OPCSOPRENABLE
                                                                                                                             VAX-11 Bliss-32 V4.0-742
COPCOM.SRCJOPRENABLE.B32:1
                                                                                                                                                                                 Page
                                                                                                                                                                                        (2)
                                  GLOBAL ROUTINE OPRENABLE_HANDLER (BUFFER_DESC) : NOVALUE =
                      Functional description:
                                             This routine is the handler for all OPRENABLE messages received by OPCOM.
                                     Input:
                                             BUFFER_DESC: The address of a quadword buffer descriptor that describes the buffer containing the message.
                                     Implicit Input:
                                             None.
                                     Output:
                                             None.
                                     Implict output:
                                             Some accounting data will be updated
                                             to reflect the receipt of the message.
                                     Side effects:
                                             None.
                                    Routine value:
                      0141
                      0142
                                             None.
                      0144
                                  BEGIN
                                                                                                      ! Start of OPRENABLE_HANDLER
                      0146
                                  MAP
                      0148
0149
0150
0151
0152
0153
0154
0156
0157
0158
0159
                                             BUFFER_DESC
                                                                    : $ref_bblock;
                                  LOCAL
                                                                      $desc_block,
$bblock [OPC$K_MAXREAD],
VECTOR [5,LONG],
$ref_bblock,
VECTOR [3],
                                             STATUS_BESC
STATUS_BUF
MESSAGE_VECTOR
                                                                                                         Descriptor for status message
! Buffer for status message
                                                                                                         Message vector
                                                                                                         Pointer to user request text
Arguement list
1 if ENABLE, 0 if DISABLE
                                             MSG
                                             ARG LIST
ENABLE FLAG
FULL DISABLE
FULL ENABLE
FOUND
                                                                       LONG.
                                                                        LONG.
                                                                                                         Boolean
                                                                        LONG.
                                                                                                         Boolean
                                                                        LONG.
                                                                                                         Boolean
                      0160
                                             LOST_COVERAGE
                                                                        LONG,
                                                                                                         Boolean
                         161
                                             OCD
                                                                       Sref bblock.
                                                                                                         OCD structure
                                                                       $ref_bblock,
$ref_bblock,
                                             ROCB
OPER_ROCB
                                                                                                         Request Context Block
                                                                                                         Known operator RQCB
                                             STATUS
                                                                      LONG:
                                 FULL_ENABLE = FALSE;
```

OP

VO

OP!

vo

20

72

-

```
OP O
```

```
OPCSOPRENABLE V04-000
                                                                                                    16-Sep-1984 01:41:12
14-Sep-1984 12:50:52
                                                                                                                                         VAX-11 Bliss-32 V4.0-742
COPCOM.SRCJOPRENABLE.B32:1
                                                                                                                                                                                                 Page
                                     (NOT .$bblock [RQCB [RQCB L PRIVMASK1], PRV$V OPER])))
OR ((.RQCB [RQCB L SENDERUIC] NEQ .RQCB [RQCB L UIT]) AND
(NOT .$bblock [RQCB [RQCB L PRIVMASK1], PRV$V OPER]))
    ! Issuing a request on another's behalf
! requires OPER privilege.
                                     THEN
                                            MESSAGE_VECTOR [O] = OPC$_ILLRQST;
                                                                                                                                         ! Set request status
                                        If an error was detected, notify the
                                        would-be operator and abort the request.
                                          .MESSAGE_VECTOR [O] NEQ O
                                      THEN
                                            BEGIN
                                           FORMAT_MESSAGE (.RQCB, MESSAGE_VECTOR);
NOTIFY_OPERATOR (.RQCB);
DEALLOCATE_RQCB (.RQCB);
                                            RETURN:
                                            END:
                         0298
0299
0300
                                        Finish the request.
                                      IF NOT .$bblock [RQCB [RQCB_L_RQ_OPTIONS], OPC$V_DISABLE]
                                      THEN
                                            BEGIN
                                              This is an enable request.
                                           ENABLE_FLAG = 1;
MESSAGE_VECTOR [O] = OPC$_TERMENAB;
IF NOT .FOUND
                                                                                                                 ! Set bit value
                         0308
                                                                                                                ! Set message code
                         0309
0310
0311
0312
0313
                                            THEN
    BEGIN
                                                    The operator is not known to OPCOM. An operator RQCB must be created and inserted onto the appropriate OCD
                                                     operator list so the generalized ENABLE logic will work.
                         0316
0317
0318
0319
                                                 FULL_ENABLE = TRUE;
ALLOCATE_DS (RQCB_K_TYPE, OPER_RQCB);
CH$MOVE TRQCB_K_OVERLAY_SIZE,
RQCB_ERQCB_T_OVERLAY], OPER_RQCB [RQCB_T_OVERLAY]);
OPER_RQCB_ERQCB_L_OPTIONS] = 0;
OPER_RQCB_ERQCB_L_RQ_OPTIONS] = 0;
OPER_RQCB_ERQCB_L_ATTNMASK1] = 0;
OPER_RQCB_ERQCB_L_ATTNMASK2] = 0;
I Zero mask
OPER_RQCB_ERQCB_L_ATTNMASK2] = 0;
I Zero mask
                                                                                                                                            Clone a new RQCB
                                                                                                                                          ! Copy old RQCB
                         0319
0320
0321
0322
0323
0324
0325
0327
                                                                                                                                            Zero option mask
Zero option mask
                                                                                                                                            Zero mask for enable
                                                                                                                                            Zero mask for enable
                                                     Insert the operator RQCB into the operator list in the appropriate OCD. If no OCD is found, then a new one must be created.
                                                  IF NOT FIND_OCD (.RQCB [RQCB_B_SCOPE], .RQCB [RQCB_L_UIC], OCD)
                         0330
                                                  THEN
```

FULL_DISABLE = TRUE;

OPC VO4

: R

```
OPCSOPRENABLE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      16-Sep-1984 01:41:12
14-Sep-1984 12:50:52
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         VAX-11 Bliss-32 V4.0-742
COPCOM.SRCJOPRENABLE.B32:1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             Page
V04-000
                                                                                                                                                                                                                             $bblock [OPER_RQCB [RQCB_L_OPTIONS], OPC$V_NOBRD] = .$bblock [RQCB_L_OPTIONS], OPC$V_NOBRD];
$bblock [OPER_RQCB [RQCB_L_OPTIONS], OPC$V_NOLOG] = .$bblock [RQCB_L_OPTIONS], OPC$V_NOLOG];
DEALLOCATE_RQCB (.RQCB);
REMQUE (.OPER_RQCB, RQCB);
Remove operator RQCB from list
RQCB = .OPER_RQCB;
Copy the RQCB address
OCD = .RQCB [RQCB_L_OCD];
OCD = .RQCB [RQCB_L_OCD];
ARG_LIST [0] = 1;
ARG_LIST [1] = OPER_RQCB [RQCB_L_OPER_LEN];
Set arguement count
ARG_LIST [1] = OPER_RQCB [RQCB_L_OPER_LEN];
Set operator device name
ARG_LIST [2] = OFF;
Set operator device name
SCMRRNL (ROUTIN=EXE$SETOPR, ARGLST=ARG_LIST);
END:
                                                                                                                             NANNANNEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMETENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMENTENEMETENEMENTENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEMETENEME
                                                                                                                                                                                                                                   END:
                                                                                                                                                                                              OCD = .OPER RQCB [RQCB_L_OCD];
RQCB [RQCB_L_OCD] = .OCD;
IF .FULL_ENABLE OR .FULL_DISABLE
THEN
                                                                                                                                                                                                                                  BEGIN
                                                                                                                                                                                                                                                  Send the ENABLE/DISABLE message to all CENTRAL operators
                                                                                                                                                                                                                                           Send the ENABLE/DISABLE message to all CENTRAL operators connected to this OCD. Notify the requestor even if not a CENTRAL operator. If this is a full disable, explicitly notify this operator of the DISABLE. This is necessary because the operator is no longer on the OCD's operator list, and would not be notified otherwise. Note that the message is sent only if this is the initial ENABLE or a full DISABLE for this operator. Note that the request RQCB must be used instead of the operator RQCB to avoid a race condition when a user does an enable/disable and then implicitly disables the operator. This would cause the OPER RQCB to evaporate in the middle of the NOTIFY_LISTED_OPERATORS operation.
                                                                                                                                                                                                                            RQCB [RQCB_L_ATTNMASK1] = OPC$M_NM_CENTRL;
RQCB [RQCB_L_ATTNMASK2] = 0;
RQCB [HDR V BRD] = TRUE;
MESSAGE_VECTOR [1] = 0;
MESSAGE_VECTOR [2] = RQCB [RQCB_L OPER_LEN];
MESSAGE_VECTOR [3] = .RQCB [RQCB_W USERNAMELEN];
MESSAGE_VECTOR [4] = RQCB [RQCB_T USERNAME];
FORMAT_MESSAGE (.RQCB, MESSAGE_VECTOR);
LOG_MESSAGE (.RQCB, MESSAGE_VECTOR);
LOG_MESSAGE (.RQCB);
NOTIFY_LISTED_OPERATORS (.RQCB);
If .FUEL_DISABLE
THEN
NOTIFY_OPERATOR (.RQCB);
                                                                                                                                                                                                                                                                 NOTIFY_OPERATOR (.RQCB);
                                                                                                                                                                                                               Since the operator status has changed, send an operator status message to the operator. This does not have to be done in the case of a full DISABLE.
                                                                                                                                                                                                              If the operator was implicitly disabled during a NOTIFY_LISTED_OPERATORS operation, then do not send the message, as it would cause the user to think that the enable operation had completed. This does not close the timing window, but only makes it's occurrance less annoying. A simple test for the implicit disable is to check the sequence number on the operator RQCB. If it is zero, it means that operator has been disabled, and the RQCB deallocated.
                                                                                                                                                                                                                and the RQCB deallocated.
```

VO

............

.EXTRN .EXTRN **EXTRN**

.EXTRN

DUMP_LOG_FILE, FIND_OCD

**1

(2)

04

19

					1	E 4 6-Sep-19 4-Sep-19	84 01:41 84 12:50	:12 VAX-11 Bliss-32 V4.0-742 Pa	ge 10
							EXTRN EXTRN EXTRN EXTRN EXTRN EXTRN EXTRN	FIND OPERATOR, FORMAT MESSAGE INTERPRET MASK, LOG MESSAGE NOTIFY_LISTED OPERATORS NOTIFY_OPERATOR UPD OPER CONTEXT VALID OPERATOR, WRITE LOG_FILE GLOBAL STATUS, EXESSETOPR SYSSCMERNL	
							.PSECT	\$CODE\$, NOWRT, 2	
			()FFC	00000		.ENTRY	OPRENABLE_HANDLER, Save R2,R3,R4,R5,R6,R7,-	: 0110
	5B 5E	00000000G F5CC	00 CE 59	9E 9E 7C	00002 00009 0000E		MOVAB CLRQ	R8, R9, R10, R11 EXESSETOPA, R11 -2612(SP), SP FULL DISABLE aBUFFER_DESC, #68	0167
0044	8F	04	BC 01	B1 1E 04	00010 00016 00018		CMPW BGEQU RET	aBUFFER_DESC. #68	0172
00006	CF 01	04	5E AC 02 50	DD DB E804	00019 0001B 0001E 00023 00026		PUSHL PUSHL CALLS BLBS	SP BUFFER DESC #2. CHECK REQUEST STATUS, 25	0179
	56		6E 56	DO	00027	28:	RET MOVL	RQCB, R6	0188
00006	CF 03	04	AC 02 50	DD DD FB E8	0002C 0002F 00034		PUSHL PUSHL CALLS BLBS	R6 BUFFER_DESC #2. VACID_OPERATOR R0. 4\$ 22\$	
		04	16C AE	9f	00037 0003A	38: 48:	BRW PUSHAB	OPER_RQCB	0200
0000G	CF 52 23		56 02 50 52	FB DO E9	0003D 0003F 00044 00047		PUSHL CALLS MOVL	#2, FIND OPERATOR RO, FOUND	0209
08	AE 50 02	24 08 08	A6 AE A0 D7	DO DO 91	0004A 0004F 00053		BLBC MOVL MOVL CMPB	FOUND 6\$ 36(R6), OCD OCD, RO 11(R0), #2	0208 0211 0212
6A	A6	26	AO	12 B1	00057		BNEQ	5\$ 38(RO), 106(R6)	0214
	03	08	D7 A0 07	12	0005E 00060	58:	BNEQ CMPB	3\$ 11(R0), #3	0215
68	A6	24	AO	12 01 12	00064		BNEQ	65	0216
		18	CA	12 70	0006B	68:	CLRQ	MESSAGE_VECTOR	•
78 58	A6		01	7C E1 BA E9 E0	00070 00075 00079		BBC BICB2	#1, 120TR6), 7\$ #2, 88(R6)	0231
	1E A6	50	A6 06	E9 FO	00079 0007b	7\$:	BLBC BBS	93(R6), 8\$ #6, 52(R6), 8\$	0239
34 50	A6 50	50	01 56 A0	8A DO	0007b 00082 00086 00089		BICB2 MOVL TSTL BNEQ TSTL	3\$ MESSAGE VECTOR #1, 120(R6), 7\$ #2, 88(R6) 93(R6), 8\$ #6, 52(R6), 8\$ #1, 93(R6) R6, R0 92(R0) 8\$	0223 0231 0233 0239 0240 0251 0255
		60	OD	D5 12 D5	0008C		BNEQ	8\$ 96(R6)	
18	AE	00058070	A6 08 8F	D5 12 D0	00091		BNEQ	8\$ #360572, MESSAGE_VECTOR	0257

OP

MOYL

INCW

MOVL

MOVAB MOVL

PUSHAB

PUSHL

#1 ARG_LIST 124(R1) ARG_LIST+4 #1 ARG_LIST+8 ARG_LIST

24

0C 10 14

AE

46

7C

00

0017C

VO

OPCSOPRENA V04-000	BLE						G 4 16-Sep-1984 01:41:12 VAX-11 Bliss-32 V4.0-742 14-Sep-1984 12:50:52 [OPCOM.SRC]OPRENABLE.B32;1	Page 12
				0000000G	00			:
				18	24 AE	0005807C 1 C 1 8	50 EQ 00187 208. PLPC PO 238	0301 0342 0348 0349 0350
				00006	CF	10	8f DO 001BA	
				00006	CF		56 DD 0019F PUSHL R6 01 FB 001A1 CALLS #1, NOTIFY_OPERATOR 56 DD 001A6 2%: PUSHL R6 01 FB 001A8 CALLS #1, DEALLOCATE_RQCB	0351
				0000G	CF		01 FB 001A8 CALLS W1. DEALLOCATE RQCB	0352
				18	AE 52	00058011	58 D4 001AE 238: CLRL ENABLE_FLAG	0344 0360 0361 0368
				00006	7E CF 53	04	03 FB 001C2 CALLS #3, CLUSMSG ROCH SEND	0379
	7E	58	A2	00006	7E 01 CF	50	OB DD 001BD PUSHL #11 01 CE 001BF MNEGL #1, -(SP) 03 FB 001C2 CALLS #3, CLUSMSG RQCB_SEND AE DO 001C7 MOVL OPER_RQCB, R3 A2 7D 001CD MOVQ 92(R2), -(SP) 00 EF 001D1 EXTZV #0, #1, 88(R2), -(SP) 04 FB 001D7 CALLS #4, UPD OPER CONTEXT 50 DO 001DC MOVL R0, LOST COVERAGE 01 E1 001DF BBC #1, 88(R2), 25\$ 58 FO 001E4 INSV ENABLE_FLAG, #1, #1, 88(R3) 55 12 001ED BNEQ 26\$ A3 D5 001EF TSTL 92(R3) 55 12 001F2 BNEQ 26\$ 01 DO 001F4 MOVL #1, FULL DISABLE 01 EF 001F7 EXTZV #1, #1, 84(R2), R0	0377 0376
58	A3		06 01	58	A2 01	50	50 DO 001DC MOVL RO, LOST COVERAGE 01 E1 001DF BBC #1, 88(R2), 258 58 FO 001E4 INSV ENABLE_FLAG, #1, #1, 88(R3) A3 D5 001EA 258: TSTL 92(R3) 55 12 001ED BNEQ 26\$	0384 0386 0391
	50	£,	42		59 01	60	A3 D5 001EF TSTL 96(R3) 50 12 001F2 BNEQ 265 01 D0 001F4 MOVL #1, FULL DISABLE	0394 0395
54 54	50 A3 A3	54	A2 01 01		01	54	50 12 001F2 BNEQ 263 01 00 001F4 MOVL W1, FULL DISABLE 01 EF 001F7 EXTZV W1, W1, 84(R2), R0 50 F0 001FD INSV R0, W1, W1, 84(R3) A2 F0 00203 INSV 84(R2), W0, W1, 84(R3) 52 DD 0020A PUSHL R2 01 FB 0020C CALLS W1, DEALLOCATE_RQCB 63 OF 00211 REMQUE (R3), RQCB	0395 0396 0397
				0000G	6E 6E 50	04	01 FB 0020C CALLS #1, DEALLOCATE_RQCB 63 OF 00211 REMQUE (R3), RQCB AE DO 00214 MOVL OPER_RQCB, RQCB	0398 0399
				08	AE 50	24 08 46	AC DO 00218 MOVL RQCB, RO AC DO 0021B MOVL 36(RO), OCD AE DO 00220 MOVL OCD, RO AC BZ 00226 DECH ZO(RO)	0400
		10	AE	00	AE	0000007C 14 0C	A3 D5 001EA 25%: TSTL 92(R3) A3 D5 001EF TSTL 96(R3) D5 001EF TSTL 96(R3) D5 001F2 BNEQ 26% 01 D0 001F4 MOVL M1, FULL DISABLE 01 EF 001F7 EXTZV M1, M1, 84(R2), R0 A2 F0 00203 INSV 84(R2), M0, M1, 84(R3) D5 DD 0020A PUSHL R2 CALLS M1, DEALLOCATE_RQCB A6 D0 00214 MOVL OPER_RQCB, RQCB A6 D0 00218 MOVL RQCB, RQCB A6 D0 00218 MOVL RQCB, RQCB A6 D0 00220 MOVL OCD, RO A6 D0 00227 MOVL M1, ARG_LIST A6 D4 00235 AE D4 00235 AE D4 00235 PUSHAB ARG_LIST A6 D4 00236 PUSHAB ARG_LIST A7 ON	0402 0403 0404 0405
				00000000G 08 24	00 52 AE		58 DD 0023B PUSHL R11 02 FB 0023D CALLS #2, SYS\$CMKRNL 6E 7D 00244 26\$: MOVQ RQCB, R2 A3 DO 00247 MOVL 36(R3), OCD AE DO 0024C MOVL OCD, 36(R2) 5A E8 00251 BLBS FULL_ENABLE, 27\$ 59 E9 00254 BLBC FULL_DISABLE, 28\$ 01 7D 00257 27\$: MOVQ #1, 92(R2) 02 88 0025B BISB2 #2, 40(R2)	0409
					95 95	24 08	A3 D0 00247 MOVL 36(R3), OCD AE D0 0024C MOVL OCD, 36(R2) 5A E8 00251 BLBS FULL_ENABLE, 27\$ 59 E9 00254 BLBC FULL_DISABLE, 28\$ 01 7D 00257 27\$: MOVQ #1, 92(R2) 02 88 0025B BISB2 #2, 40(R2)	0409 0410
				5 C	VS VS		01 7D 00257 278: MOVQ #1, 92(R2) 02 88 0025B BISB2 #2, 40(R2)	0426 0428

OP(

OPCSOPRENABLE V04-000					16· 14·	-Sep-1 -Sep-1	984 01:41: 984 12:50:	:12	Page 13 (2)
	20 24 28	AE AE AE	1 C A A A A A A A A A A A A A A A A A A		04 0025F 9E 00262 3C 00267 9E 0026C 9F 00271			MESSAGE_VECTOR+4 124(R2), MESSAGE_VECTOR+8 116(R2), MESSAGE_VECTOR+12 60(R2), MESSAGE_VECTOR+16 MESSAGE_VECTOR	: 0429 : 0430 : 0431 : 0432 : 0433
	00006	CF	5	2	DD 00274 FB 00276		CALLS	R2 M2, FORMAT_MESSAGE R2 M1, LOG_MESSAGE	
	00006	CF	Š		FB 0027B		CALLS	M1. LOG_MESSAGE	0434
	00006	CF OA	Š		DD 00282 FB 00284 E9 00289 DD 0028C		CALLS	#1, NOTIFY LISTED OPERATORS FULL DISABLE, 28\$	0435
	00006	CF	5	2	DD 0028C		PUSHL	R/	0436 0438
	00000	4E	OC A	3	FB 0028E E8 00293 D5 00296	285:	BLBS	#1, NOTIFY OPERATOR FULL DISABLE, 29\$ 12(R3) 29\$	0453
	F B F C 18	AD AD AE 000	0A00 8 2C A 0580C3 8		13 00299 3C 00298 9E 002A1 D0 002A6 D4 002AE 9E 002B1 9E 002B6		BEQL MOVZWL MOVAB MOVL	298 #2560, STATUS DESC STATUS BUF, STATUS DESC+4 #360643, MESSAGE_VECTOR MESSAGE_VECTOR+4 124(R2), MESSAGE_VECTOR+8 STATUS_DESC, MESSAGE_VECTOR+12	0460 0461 0462
	20 24	AE	7C A F8 A F8 A	2000	9f 002BB 9f 002BE		CLRL MOVAB MOVAB PUSHAB PUSHAB	SIMIUS NEST	0462 0463 0464 0465
	0000G CF 18	CF 18	0A00 8 2C 3580C3 8 1C 7C A F8 A F8 A 5C 05	330	OF DOOLE		PUSHAB	STATUS_DESC 92(R3) #3. INTERPRET_MASK RO. 298 MESSAGE_VECTOR	0469
	00006	CF	Õ	Ž	DD 002CF FB 002D1		PUSHL CALLS PUSHL	#2, FORMAT_MESSAGE	0470
	00006	CF	Õ		DD 00206 FB 00208 DD 00200		CALLS	M1. LOG_MESSAGE	0470
	00006	CF 04		-	FB 002DF E9 002E4	298:	CALLS PUSHL CALLS BLBC CLRL	#1, LOG_MESSAGE #2 #1, NOTIFY OPERATOR FULL_ENABLE, 30\$ 128(R2) R2	0471 0481 0483
	00006	CF 08	0080 C 50 0080 A	2	D4 002E7 DD 002EB : FB 002ED E9 002F2 DD 002F5	30\$:	BLBC	R2 #1. DEALLOCATE_RQCB LOST_COVERAGE, 31\$ OCD	0484 0491 0493
	00006	CF	Ô		FB 002F8 04 002FD	315:	CALLS	#1. CHECK_OPER_COVERAGE	0495

OP!

; Routine Size: 766 bytes, Ro

Routine Base: \$CODE\$ + 0000

Sref_bblock,

: \$ref_bblock.

OCD structure

Request Context Block

Known operator RQCB

0550

OCD

ROCB

OPER ROCB

VO

```
STATUS
                                                               : LONG:
FULL_ENABLE = FALSE;
FULL_DISABLE = FALSE;
                                Check the version number of the message. If the message is from any other version,
                                simply ignore it.
                                 .CLM [CLM_B_DS_VERSION] NEQ CLMRQCB_K_DS_VERSION
                              THEN
                                   RETURN DUMP_LOG_FILE (.BUFFER_DESC, %ASCID 'CLM_OPRENABLE mismatch');
                                Allocate an RQCB and convert the message RQCB into the new RQCB
                              IF NOT CLUSMSG_CONV_CLM_RQCB (.CLM, RQCB)
                              THEN
                                   RETURN DUMP_LOG_FILE (.BUFFER_DESC, ascid_INVALIDRQCB);
                                See if the operator is already known to OPCOM. This entails scanning down the appropriate operator list and comparing the device names for equality. FIND_OPERATOR will set RQCB_LQCD] if it
                  0572
0573
0574
0575
0576
0577
                                 finds a match.
                              FOUND = FIND_OPERATOR (.RQCB, OPER_RQCB);
                                If the operator is known to OPCOM, then the requestor's UIC must agree with the UIC of the known operator. If it does not, then dismiss the request without notifying the requestor, as this would only allow a user to cause an operator to be plauged with error messages for a request he
580
581
583
583
584
585
586
588
591
593
593
593
593
593
593
593
                                did not send.
                             IF .FOUND THEN
                                   BEGIN
                                  Get the OCD address
If GROUP OCD, the group fields
of the UIC must be the same for both
                                                                                                              the known operator and the requestor.
                                                                                                              If USER OCD, the UICs must be equal.
                                   THEN
                                                                                                            ! Dismiss the request
                                        WRITE_LOG_FILE (%ASCID 'bad uic in clmrqcb');
DEALLOCATE_RQCB (.RQCB);
                                         RETURN:
                                         END:
600
                             MESSAGE_VECTOR [0] = 0;
MESSAGE_VECTOR [1] = 0;
                                                                                                            ! Assume no error
601
602
                                finish the request.
604
605
                              IF NOT .$bblock [RQCB [RQCB_L_RQ_OPTIONS], OPC$V_DISABLE]
606
                              THEN
                                   BEGIN
608
609
                                      This is an enable request.
610
                                                                                                ! Set bit value
                                   ENABLE_FLAG = 1;
```

Update the OCD count vector for each bit present in the bit mask. The count will be decremented for a DISABLE, incremented for an ENABLE. Also update the OCD operator intrest mask, and the corresponding intrest mask in the operator RQCB. Note if there was a potential lose of operator coverage on any outstanding requests.

665 666 66

668

OP VO

```
OPCSOPRENABLE
                                                                                                       16-Sep-1984 01:41:12
14-Sep-1984 12:50:52
                                                                                                                                             VAX-11 Bliss-32 V4.0-742
[OPCOM.SRC]OPRENABLE.B32:1
V04-000
                                      LOST_COVFRAGE = UPD_OPER_CONTEXT (.$bblock [RQCB [RQCB L RQ_OPTIONS], OPC$V_DISABLE],
.RQCB [RQCB L ATTNMASKT],
.RQCB [RQCB L ATTNMASK2],
.OPER_RQCB
    669
                         0668
0669
0670
0671
0672
0673
0674
0676
0677
0678
0681
0683
0684
0685
0686
    671
672
673
674
675
                                         Set the PERMOPER bit as desired.
                                           .$bblock [RQCB [RQCB_L_RQ_OPTIONS], OPC$V_PERMOPER]
    678
                                            $bblock [OPER_RQCB [RQCB_L_RQ_OPTIONS], OPC$V_PERMOMER] = .ENABLE_FLAG:
    680
    681
                                         If the OPER_RQCB attention mask went to 0, then disable the operator terminal.
    682
                                      IF (.OPER_RQCB [RQCB_L_ATTNMASK1] EQL 0) AND (.OPER_RQCB [RQCB_L_ATTNMASK2] EQL 0)
                                      THEN
    685
                                            BEGIN
                                            686
687
    688
    689
    690
                         0688
    691
                         0689
                         0690
0691
    692
    693
                         0692
0693
    694
                                            END:
    695
                                      OCD = .OPER RQCB [RQCB L OCD];
RQCB [RQCB [ OCD] = .OCD;
IF .FULL_ENABLE OR .FULL_DISABLE
    696
697
                         0694
                         0695
   698
699
700
                         0696
                         0697
                                      THEN
                         0698
                                            BEGIN
    701
702
703
704
705
706
707
708
709
710
                         0699
                         0700
                                                Send the ENABLE/DISABLE message to all CENTRAL operators connected to this OCD. Do not notify
                         0701
                                                the requestor, she is on another node.
                         0702
                                           RQCB [RQCB_L_ATTNMASK1] = OPC$M_NM_CENTRL;
RQCB [RQCB_L_ATTNMASK2] = 0;
RQCB [HDR V BRD] = TRUE;
MESSAGE_VECTOR [1] = 0;
MESSAGE_VECTOR [2] = RQCB [RQCB_L_OPER_LEN];
MESSAGE_VECTOR [3] = .RQCB [RQCB_D USERNAMELEN];
MESSAGE_VECTOR [4] = RQCB [RQCB_T_USERNAME];
FORMAT_MESSAGE_(.RQCB, MESSAGE_VECTOR);
LOG_MESSAGE_(.RQCB, MESSAGE_VECTOR);
NOTIFY_LISTED_OPERATORS (.RQCB);
END:
                         0704
                         0705
0706
0707
0708
0709
                         0710
                                            END:
                                         Deallocate the RQCB. If this was a full enable, clear the pointer to the operator device name, as both the RQCB and the OPER_RQCB point to the same device name. Deallocating the RQCB in this
    718
719
                                         case would cause the OPER_RQCB device name to evaporate.
    720
721
722
723
724
725
                                           .FULL_ENABLE
                         0720
                                      THEN
                                      PEALLOCATE_ROCB (.ROCB);
```

OP(\$0PR	ENAB	ILE												1	4 5-Sep-19 4-Sep-19	84 01:41: 84 12:50:	12 VAX-11 Bliss-32 V4.0-742 Page 52 COPCOM.SRCJOPRENABLE.832;1	18
	726 727 728 729 730 731 732 733			072 072 072 072 072 072 073	45678901	: :	.LOS	ST_CC	VERA	NGE		et an		of que		or cover at no lo	age,check nger have	the remaining requests queued to this OCD. e operator coverage. OPRENABLE_CLM_HANDLER	
20	45	40	42	41	4E	45	52	50	4.5	SE	SE	40	46	42	00000	D AAD.		<pre>\$PLIT\$,NOWRT,NOEXE,2 \CLM_OPRENABLE mismatch\<0>;</pre>	
20	7,	46	46	71	76	00	68	63	74	61	6D	73 0	69 10F00	43 60 17	0000F	P.AAB:	.LONG	17694743	
72	6D	60	63	20	6E	69	20	63	69	75 00	20	64 62	10E00 00000 61	00°	00015 00020 0002F	P.AAD:	.ADDRESS		
										00	00	0.	61 63 10E00 00000	12	0002F 00034 00038	P.AAC:		17694738	
																	.EXTRN	ASCID_INVALIDRQCB	
																		\$CODE\$,NOWRT,2	
													01	FFC	00000		.ENTRY	OPRENABLE CLM HANDLER, Save R2,R3,R4,R5,R6,-; R7,R8,R9,R10,R11 DEALLOCATE RQCB, R11 -2612(SP), SP	0496
										58 5E	F	0000G	CF	9E 9E 7C	00002 00007 0C00C		MOVAB	DEALLOCATE_ROCB, R11 -2612(SP), SP	
										52 02		08	CE 59 AC A2 06 CF	D0	0000E		MOVL	FULL_ENABLE CLM, R2 2(R2), #2	0555 0561
											(0000	06 CF	13 9F	00011		ELC OIL	P.AAA	0563
												004	10 8F	11 BB	0001C 0001E	15:	BRB	2\$ #AM <d2 sd3<="" td=""><td>0567</td></d2>	0567
								000	906	CF OD	,	20006	50	FB E8	0001E 00022 00027 0002A		BLBS	RO. 3\$	0540
								000)0G	CF	,	000G 04	8F 02 50 CF AC 02	f B	0002E 00031	2\$:	PUSHR CALLS BLBS PUSHAB PUSHL CALLS	#2, CLUSMSG_CONV_CLM_RQCB R0, 3\$ ASCID_INVALIDRQCB BUFFER_DESC #2, DUMP_LOG_FILE	0569
										57		04	AE	04 9F DO DD FB	0002E 00031 00036 00037 0003A 00040 00045 00048 00058 00058	38:	KEI	i de la companya de	0575
								000)0G	CF			57 02	DD	0003E 00040		PUSHAB MOVL PUSHL CALLS	R7 #2. FIND OPERATOR R0. FOUND	
									10	52 2F		21	50	E9	00045		MILIAT.	RO, FOUND FOUND, 6\$	0582
								(8(AE 50 02		24 08 0B	AEE 5720527 AEO 070 AO 083	DO E9 DO DO 91 12	00050		MOVL	FOUND 6\$ 36(R7) OCD OCD R0 11(R0) #2	0582 0585 0586
									5A	A7		26	07	12 B1	00058 0005A		MOVL CMPB BNEQ CMPW	43	0588
										03		08	AO	B1 12 91	0005F 00061 00065	48:	BNEU	5\$	0589

OP VO

OP

VO

OPCSOPRENABLE V04-000		B 5 16-Sep-1984 01:41:12 VAX-11 Bliss-32 V4.0-742 14-Sep-1984 12:50:52 [OPCOM.SRC]OPRENABLE.B32;1	Page 20 (3)
54 A3 54 A2 01 01	5C 60 5A 01 01 00 54 6B 6E 6E 04 08 AE 24 24 A2 08 5C A2 28 A2 1C 7C 74 28 AE 3C 18 0000G CF 0000G CF 0000G CF 0000G CF 04 0080 6B 08 0080 6B 08	A3 D5 00149 138: TSTL 92(R3) 36 12 0014c A3 D5 0014e TSTL 96(R3) 31 12 0015T BNEQ 14\$ 01 D0 0015S MOVL #1, FULL_DISABLE 01 EF 00156 EXTZV #1, #1, 84(R2), R0 50 FO 0015C INSV R0, #1, #1, 84(R3) A2 FO 0016C INSV 84(R2), #0, #1, 84(R3) C1 FB 0016B CALLS #1 DEALLOCATE_RQCB A6 D0 0017T MOVL RQCB, RQCB A6 D0 0017T MOVL RQCB, RQCB A6 D0 0017D MOVL OCD, RO A7 00 00184 14\$: MOVL OCD, RO A8 D0 00188 MOVL 36(R0), OCD A8 D0 00188 MOVL 36(R0), OCD A8 D0 00188 MOVL 36(R0), OCD A9 D0 00188 MOVL 36(R0), OCD A9 D0 00190 BS BLBC FULL_ENABLE, 15\$ D1 00190 BLBC LULD_DISABLE, 16\$ D1 7D 00198 15\$: MOVA MCCB, R2 D2 88 00195 BLBS FULL_ENABLE, 15\$ D1 00198 MOVA MOVA 116(R2), MESSAGE_VECTOR+4 D2 001AB MOVAB 124(R2), MESSAGE_VECTOR+12 D2 PS 001BA CALLS #1, LOG_MESSAGE D1 FB 001CB PUSHL R2 D1 FB 001CB CALLS #1, LOG_MESSAGE D1 FB 001CB CALLS #1, CHECK_OPER_COVERAGE	0681 0684 0685 0686 0687 0688 0689 0690 0691 0694 0695 0696 0703 0705 0706 0707 0708 0709 0710 0711 0712 0719 0721 0722 0727 0729 0731
; Routine Size: 485 bytes,	Routine Base: \$CODE\$	+ 02FE	
: 734 0732 1 : 735 0733 1 END : 736 0734 0 ELUDOM		! End of OPRENABLE	

PSECT SUMMARY

Page 21 (3)

VAX-11 Bliss-32 V4.0-742 COPCOM.SRCJOPRENABLE.B32:1

OP VO

C 5 16-Sep-1984 01:41:12 14-Sep-1984 12:50:52

Name

Bytes

Attributes

SCODES SPLITS

OPCSOPRENABLE V04-000

1251 NOVEC, NOWRT, RD , EXE, NOSHR, LCL, REL, 60 NOVEC, NOWRT, RD , NOEXE, NOSHR, LCL, REL, CON, NOPIC, ALIGN(2) CON, NOPIC, ALIGN(2)

Library Statistics

File	Total	Symbols Loaded	Percent	Pages Mapped	Processing Time
_\$255\$DUA28:[SYSLIE]LIB.L32;1 _\$255\$DUA28:[OPCOM.OBJ]OPCOMLIB.L32;1	18619 633	17	9	1000	00:01.8

COMMAND QUALIFIERS

BLISS/CHECK=(FIELD, INITIAL, OPTIMIZE)/LIS=LIS\$:OPRENABLE/OBJ=OBJ\$:OPRENABLE MSRC\$:OPRENABLE/UPDATE=(ENH\$:OPRENABLE)

1251 code + 60 data bytes 00:26.0 01:22.7 1695 Size:

Run Time: Elapsed Time: Lines/CPU Min: : Lexemes/CPU-Min: 18820 : Memory Used: 244 pages : Compilation Complete 0291 AH-BT13A-SE

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